

ABSTRACT OF THE DISCLOSURE

Two processors are provided within a radio communication unit and linked together by a connection internal to the unit comprises. A process is effectuated for detecting transmission errors between the two processors. Codes transmitted between the processors are apportioned into frames which include indicators written by the processor sending the frames. On reception
5 of the frames, the indicators are used by the receiving processor to validate the frames received. The codes transmitted may correspond to information contained in packets. The information of a packet that is intended to be transmitted via the internal connection may be placed in a single frame associated with the packet.